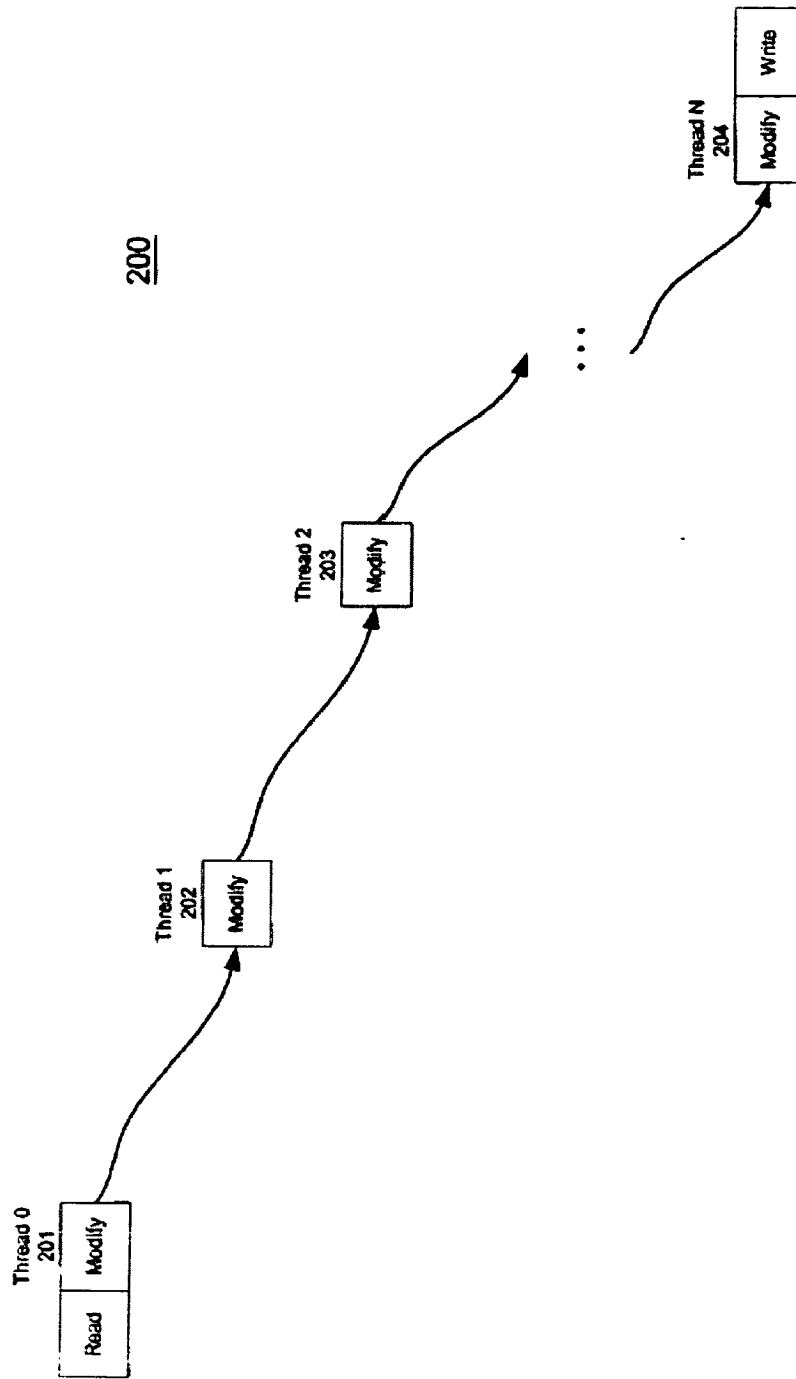
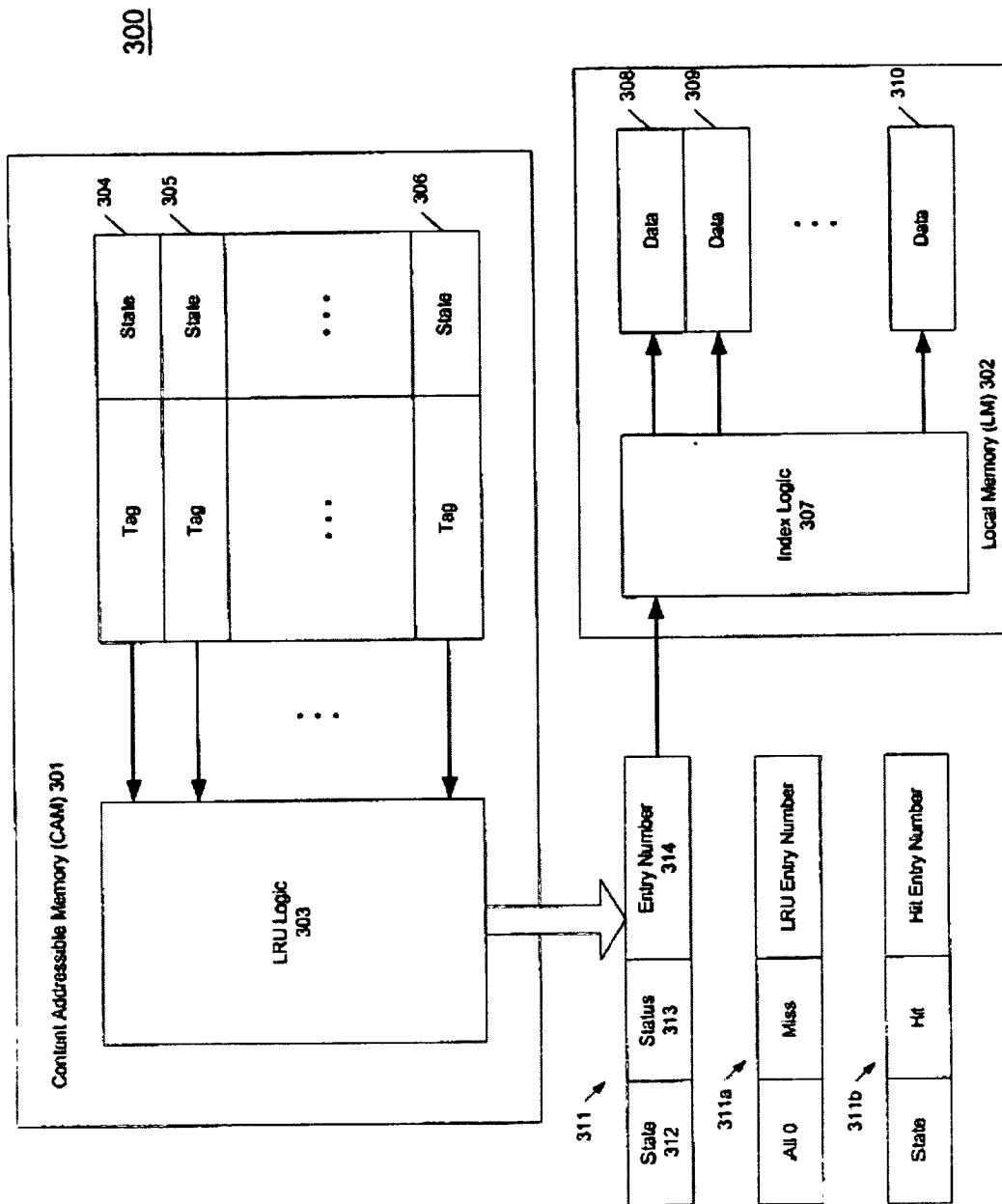


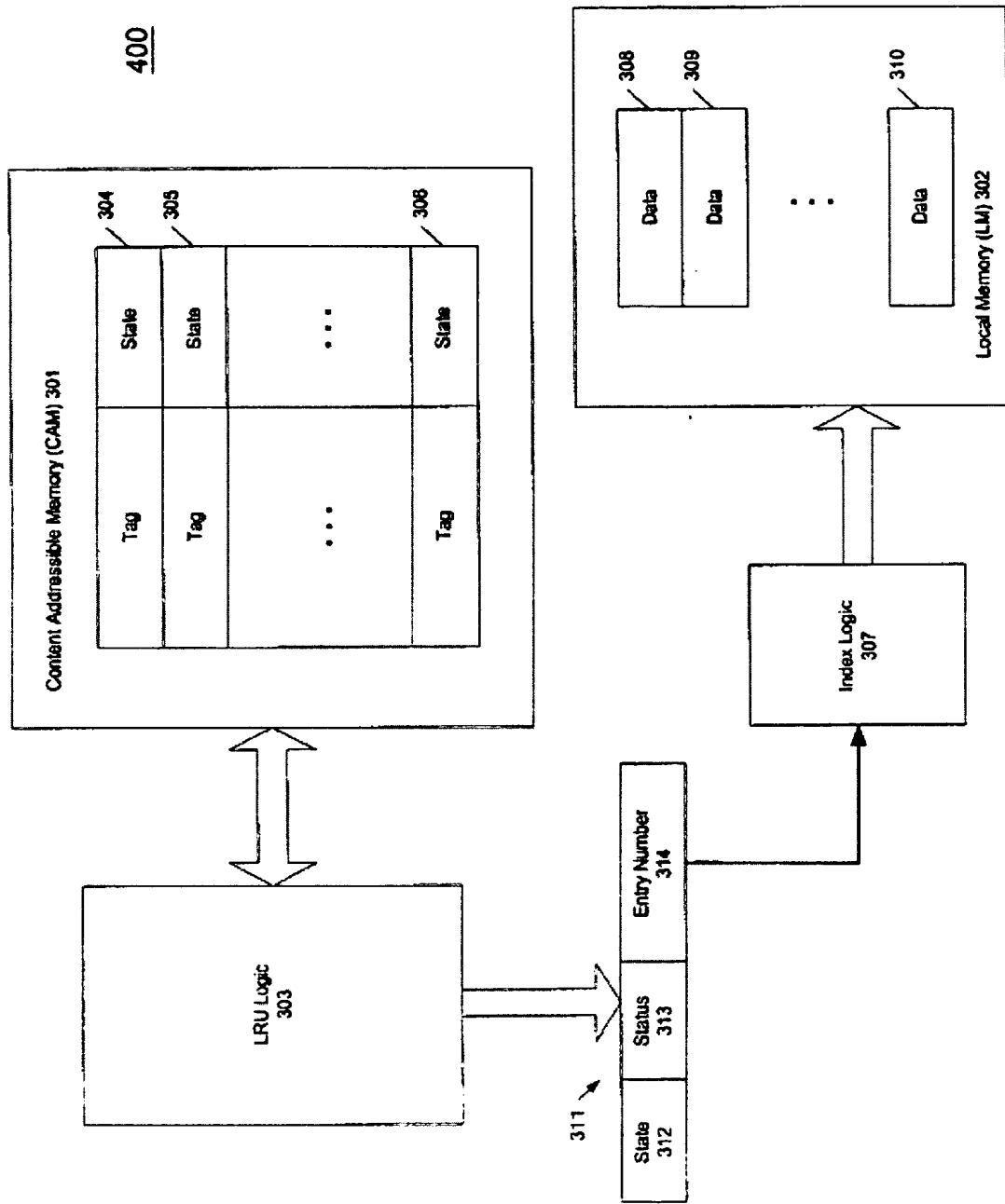
**Fig. 1**  
**(Prior Art)**



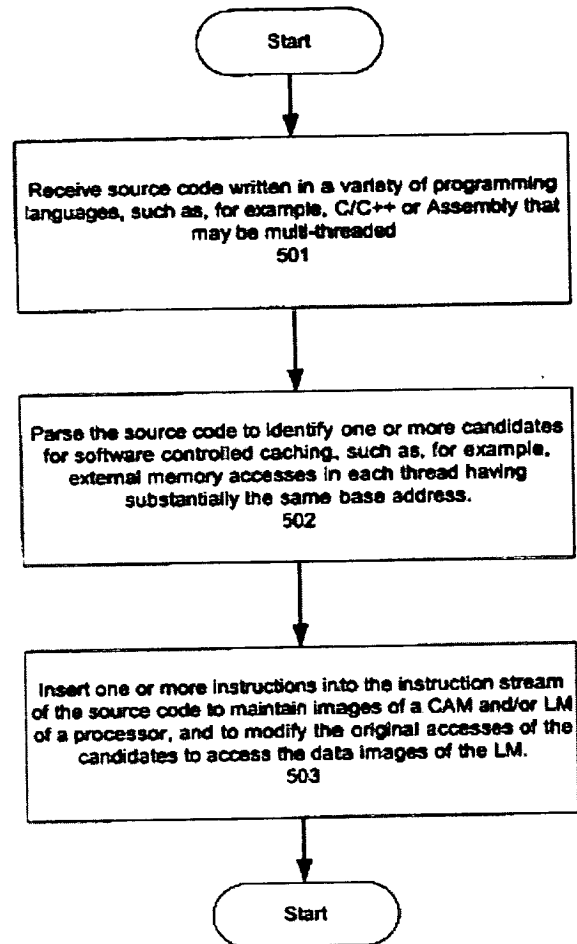
**Fig. 2**



**Fig. 3**



**Fig. 4**

500**Fig. 5**

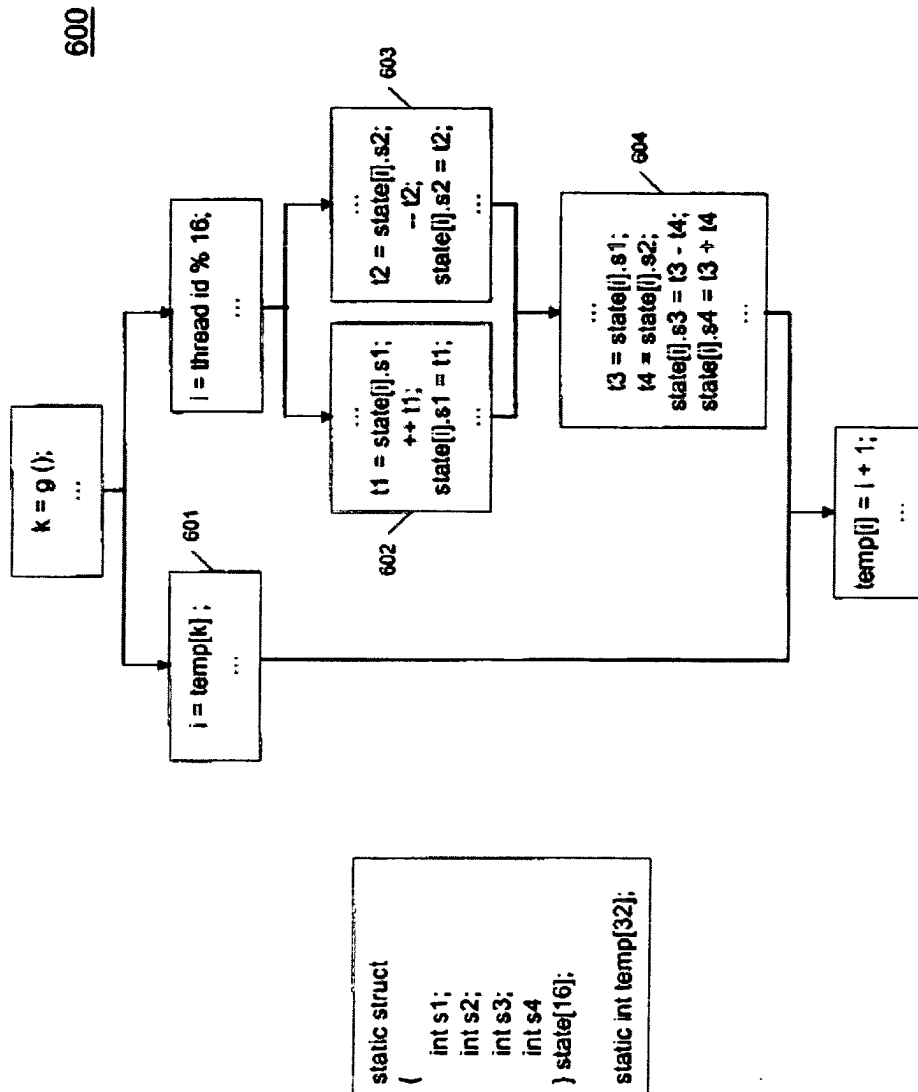


Fig. 6

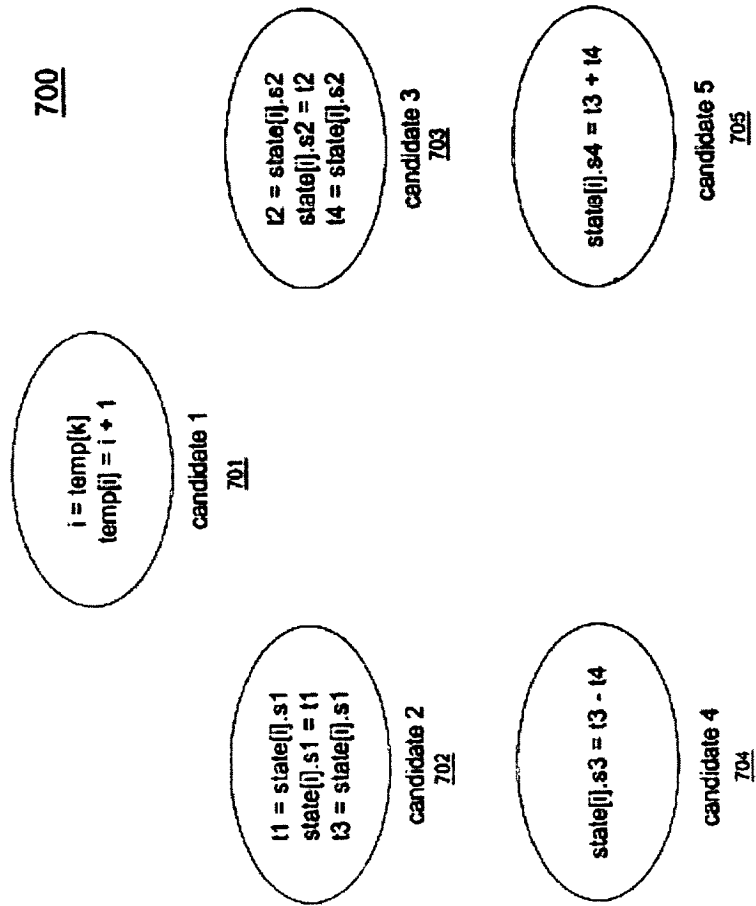
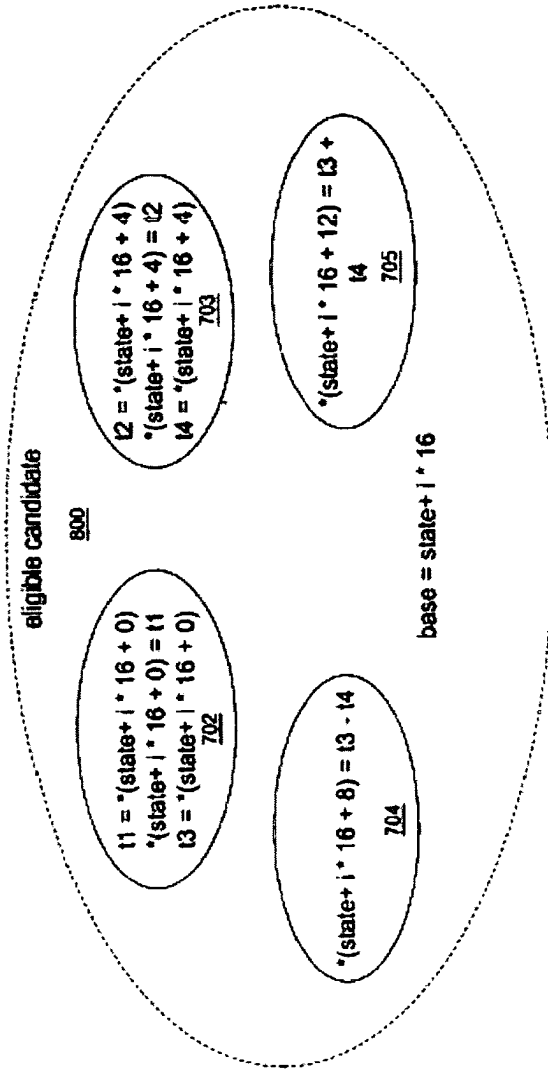


Fig. 7

ineligible candidate

$$\begin{array}{l} i = *(temp + k * 4) \\ *(temp + i * 4) = i + 1 \end{array} \quad \underline{701}$$

**Fig. 8**





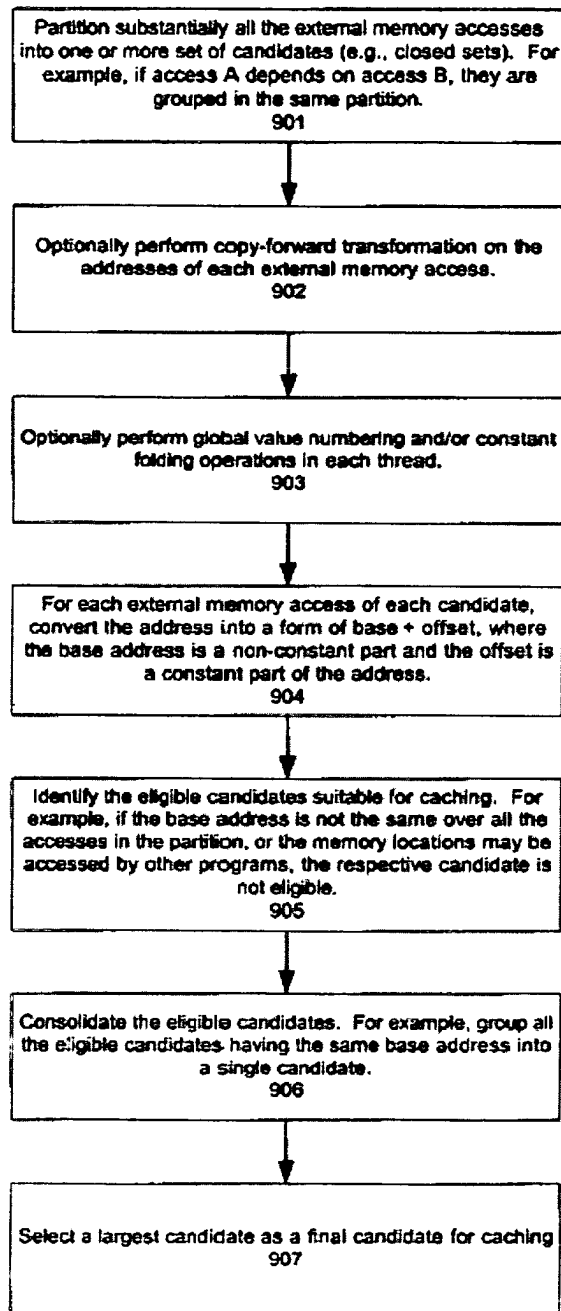


Fig. 9

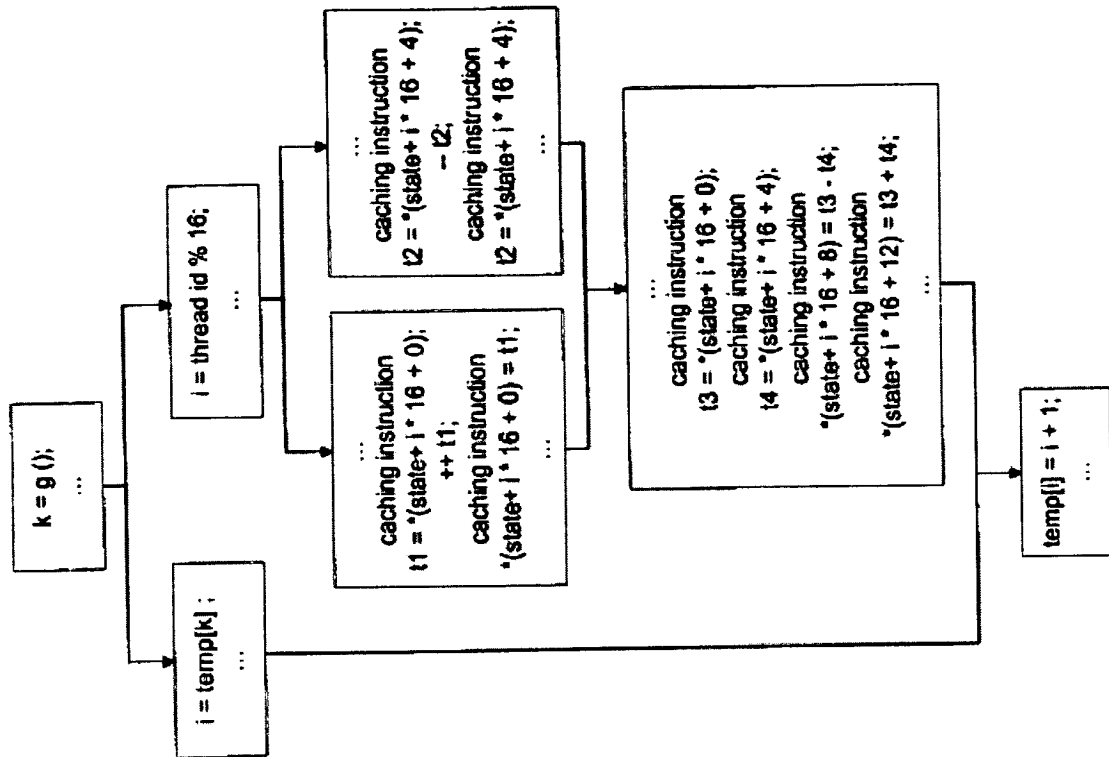
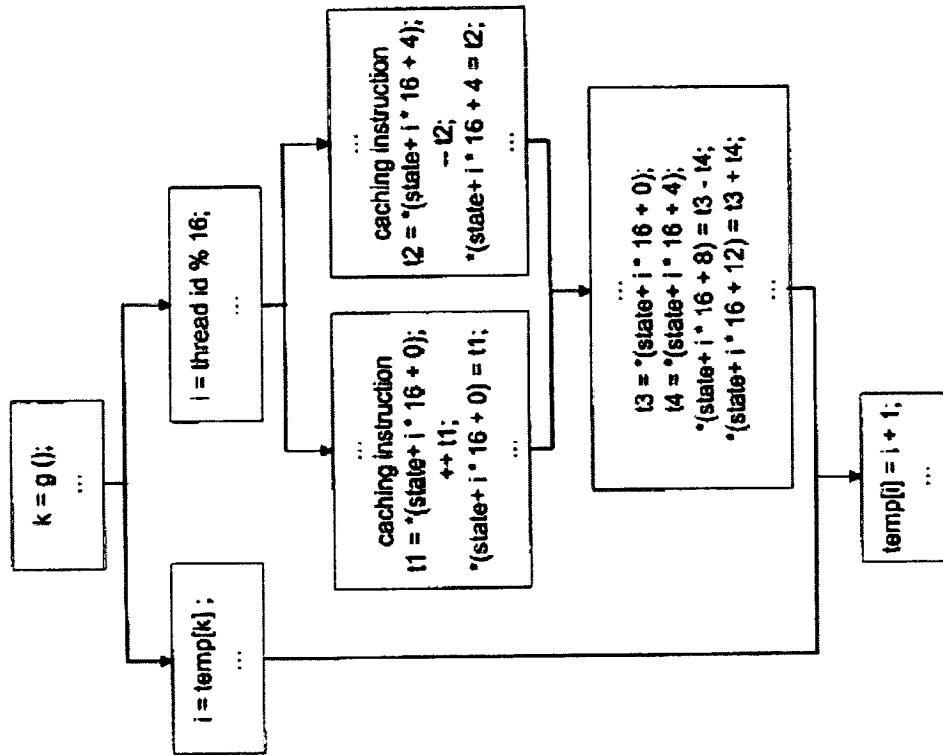


Fig. 10

1100**Fig. 11**

1200

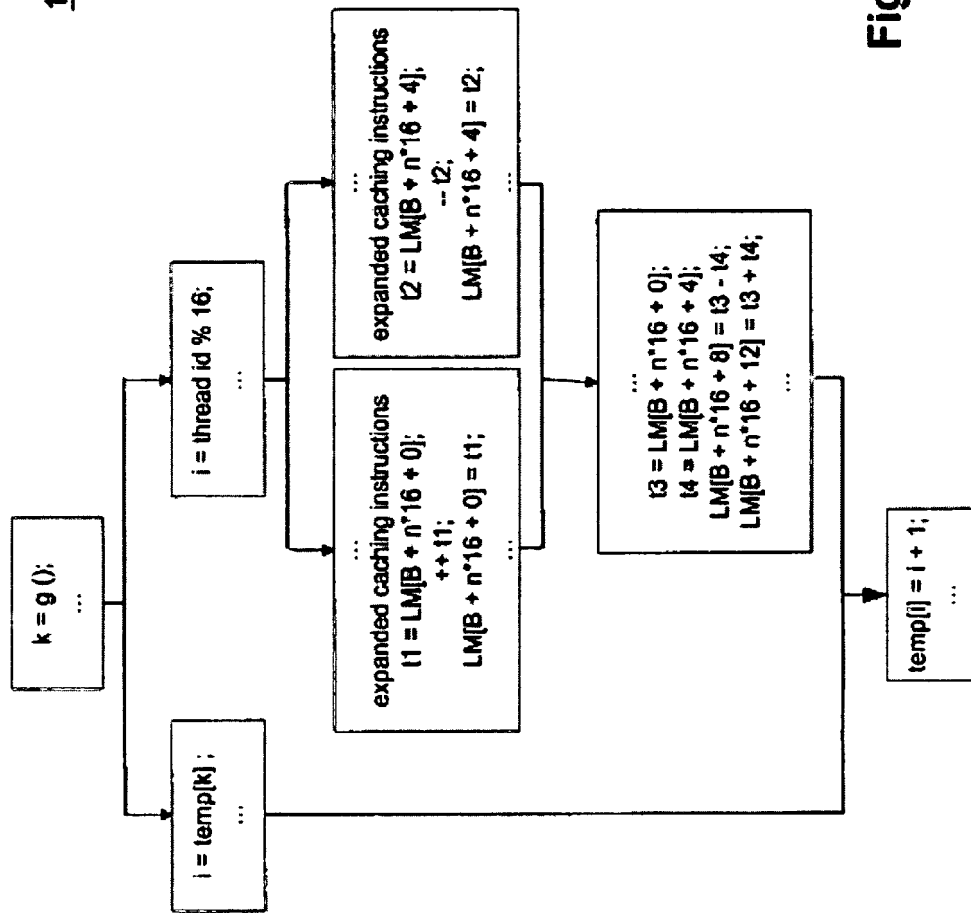
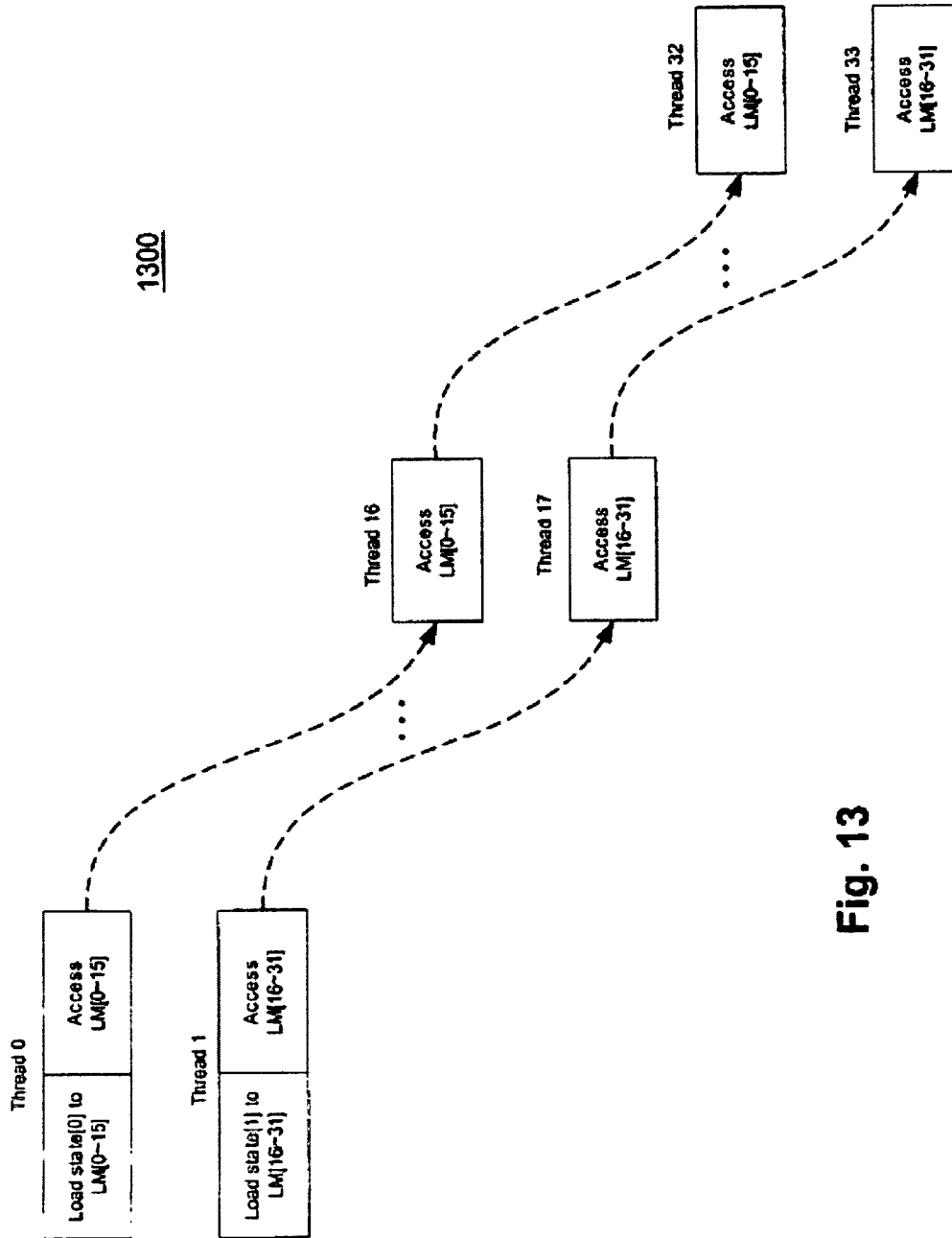


Fig. 12



**Fig. 13**

...

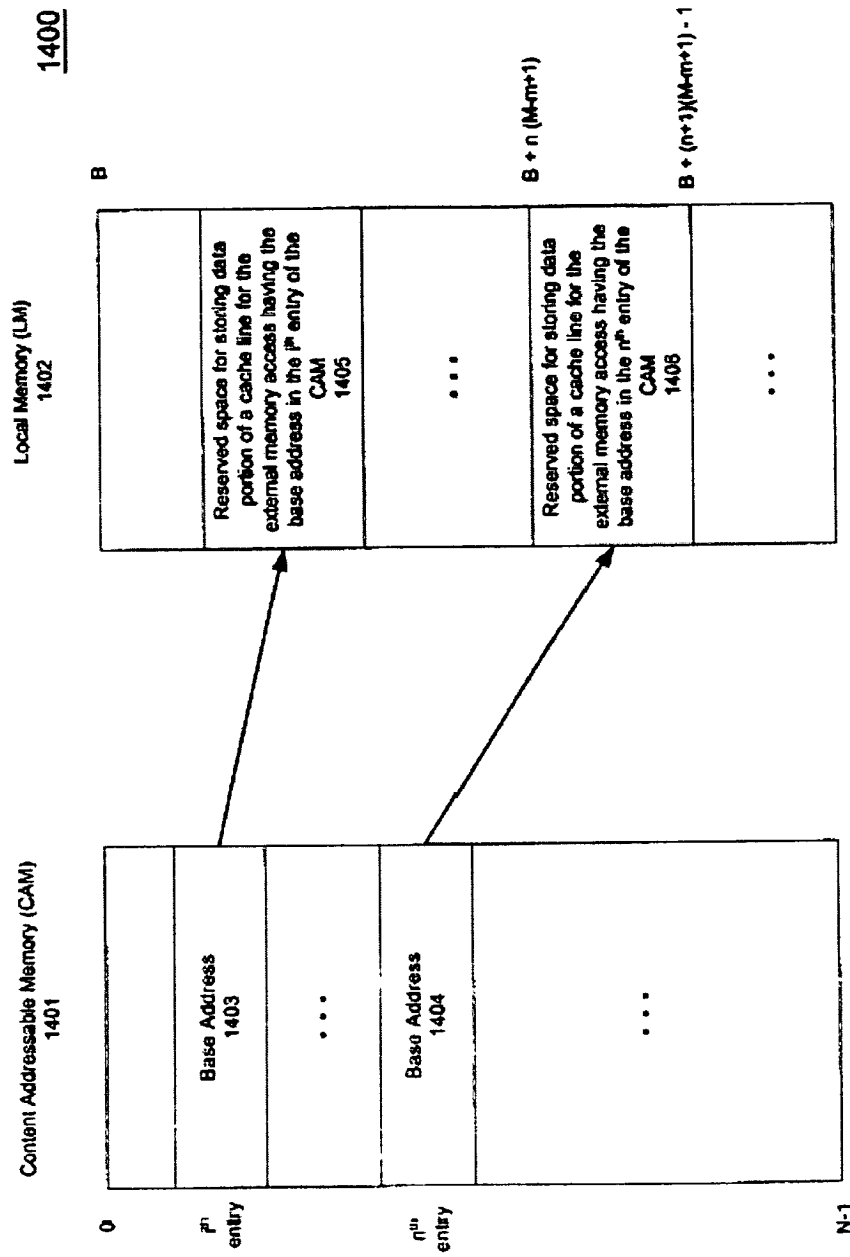
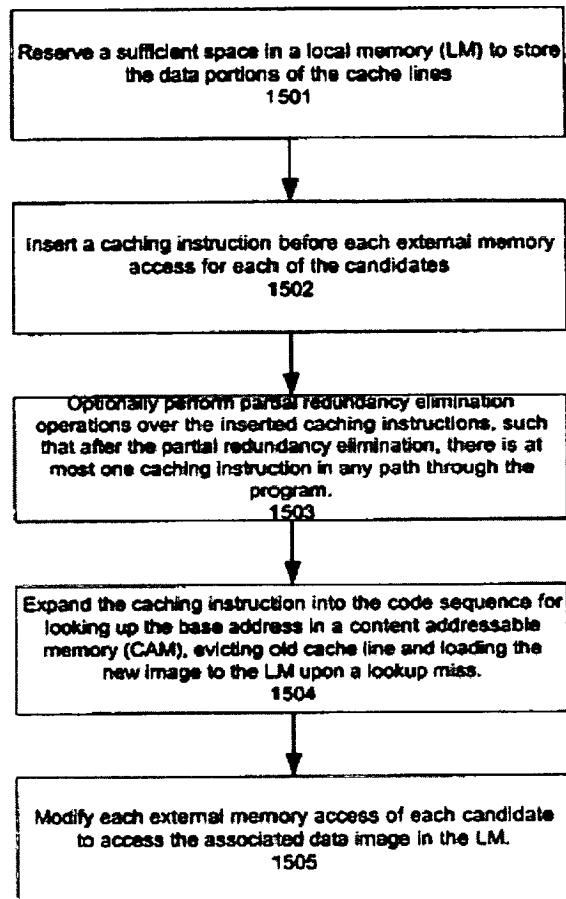


Fig. 14

1500**Fig. 15**

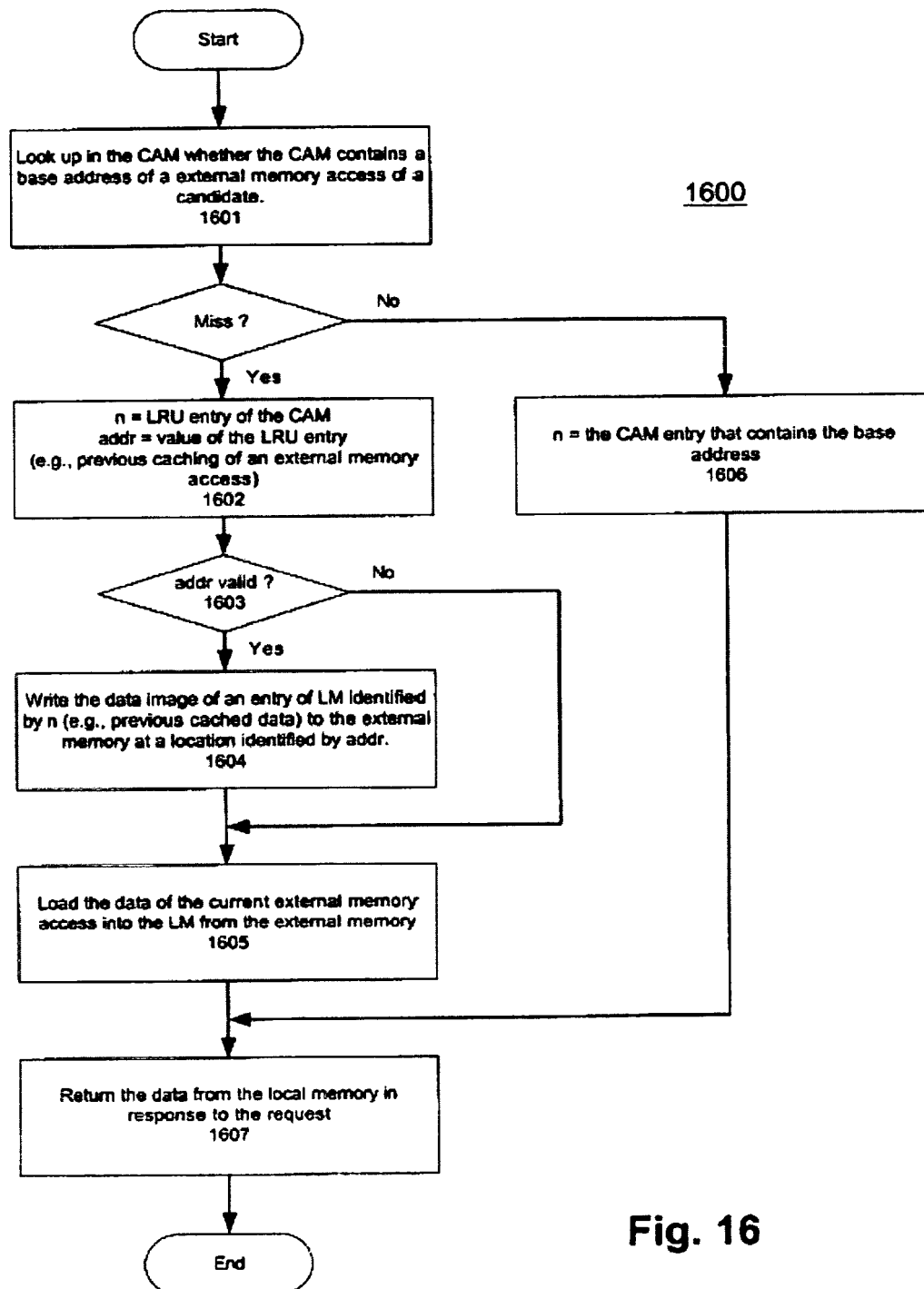


Fig. 16



```

CAM lookup for base
if (lookup miss)
{
    n = the LRU entry in CAM;
    addr = the value contained in the LRU entry in CAM;
    if (addr is valid)
    {
        write the data image in LM (from  $B + n * (M-m+1)$  to
         $B + (n+1) * (M-m+1) - 1$ ) back to external memory
        (from addr + m to addr + M);
    }
    write base to the  $n^{th}$  entry in the CAM;
    load external memory (from base + m to base + M)
    to LM (from  $B + n * (M-m+1)$  to  $B + (n+1) * (M-m+1) - 1$ );
}
else
{
    n = the associated entry in CAM containing base;
}

```

Fig. 17

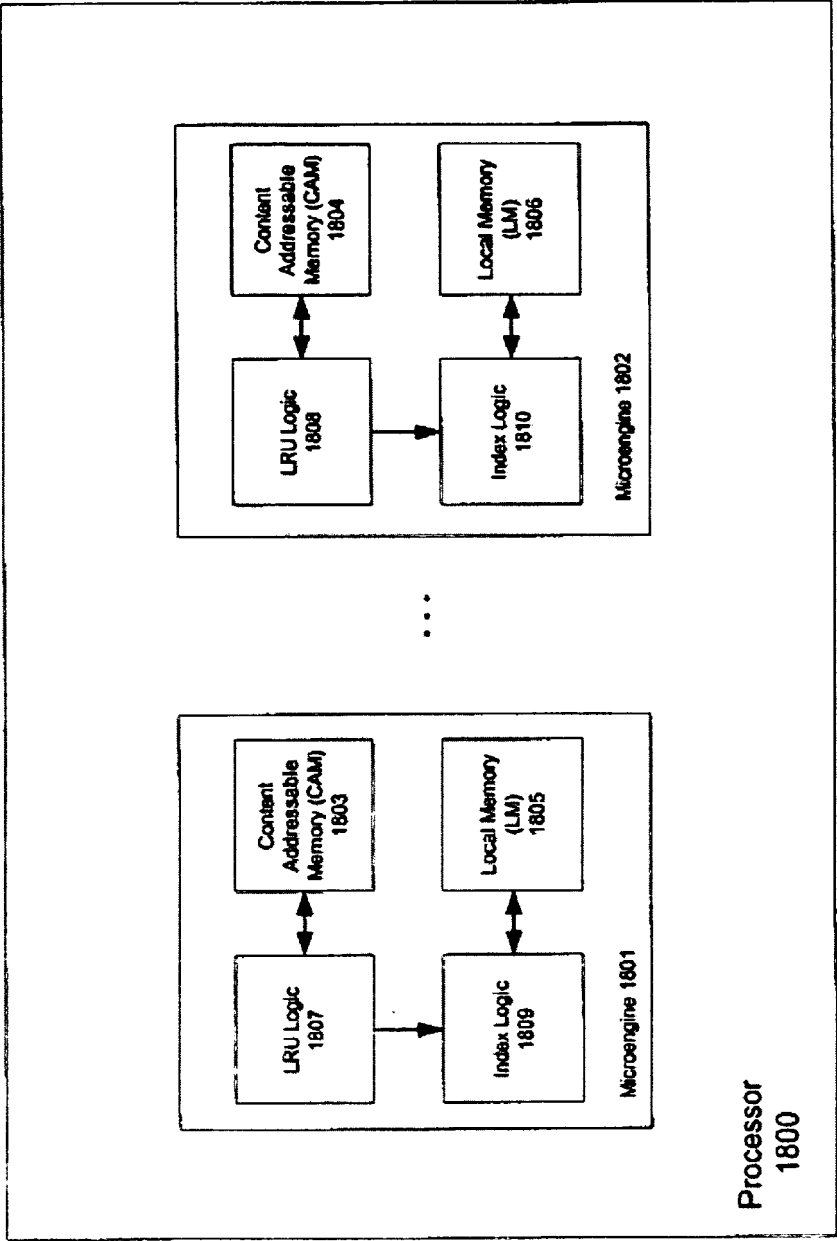


Fig. 18

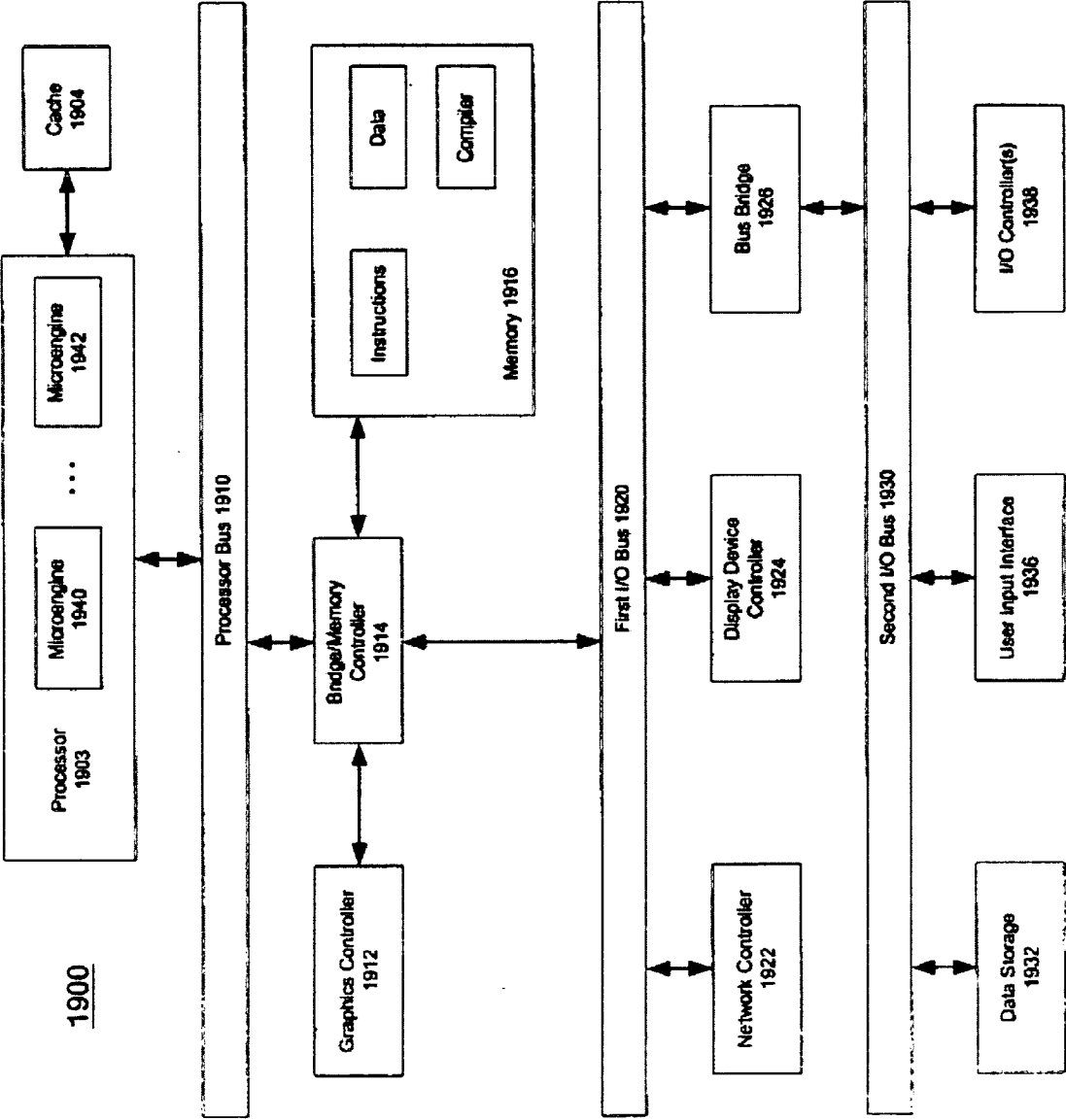


Fig. 19